

Principal Publications (Last 5 Years):

- [1] "A VLSI Implementation of a Universal Programmable Low Sensitivity Sampled Data Filter", *IEEE International Symposium on Circuits and Systems*, May 6-9 2001, Sydney Australia, 674-677 Vol 4 (With R. Pieper)
- [2] "Application of a Robust Algorithm for Predicting Freeze-out and Exhaustion Under a Variety of Nontrivial Conditions", *IEEE International Symposium on Circuits and Systems*, May 6-9 2001, Sydney Australia, 477-480 Vol 2 (With R. Pieper)
- [3] "Application of a Robust Algorithm for Predicting Freeze-out and Exhaustion Under a Variety of Nontrivial Conditions", *33rd Southeastern Symposium on System Theory*, March 2001, Ohio University, Athens Ohio, pp289-292 (With R. Pieper)
- [4] "Circuit Modeling to Predict the Performance of Forced Cooled Plate Structure," *Proceedings of the 1999 IEEE International Symposium on Circuits and Systems*, June, 1999, Orlando FL Volume: 6, pp 105-108. (With R. Pieper)
- [5] "A Robust Algorithm for Predicting Freezeout and Exhaustion Under Equilibrium Conditions," *Proceedings of the Second International Conference on Modeling and Simulation of Microsystems*, April 1999, San Juan, PR. Pp 87-90. (With R. Pieper)
- [6] "Using PSPICE to Model the Cooling Performance of Convection Surfaces," *Proceedings of the Thirtieth Southeastern Symposium on System Theory*, March 1998, Morgantown, WV, pp 85-90. (With R. Pieper)
- [7]. "Analog/ Digital Gallium Arsenide circuits and Systems Design", *Proceedings of the 40th Midwest Symposium on Circuits and Systems*, Sacramento, CA, August 1997.(with K. Shehata & D. Fouts.)
- [8]. "Dynamic Logic Families for Complementary Gallium Arsenide(CGaAs) Fabrication Processes", *Proceedings of the 40th Midwest Symposium on Circuits and Systems*, Sacramento, CA, August 1997.(with K. Shehata & D. Fouts.)
- [9]. "A GaAs Mixed Mode Switched Capacitor VLSI", *Proceedings of the 40th Midwest Symposium on Circuits and Systems*, Sacramento, CA, August 1997.(with H. Oldland .)
- [10] "Programmable Sampled Data Filter with Low Sensitivity Implementation," *Proceedings of the international Conference on Electronics, Circuits and Systems*, Rhodes, Greece, October 1996.